

WHAT IS CLAIMED

1. A stereophonic expansion circuit comprising active matrixing means including active amplifiers configured for matrix processing (L + R) and (L-R) signals for deriving L and R stereophonic signals.

5 2. A stereophonic expansion circuit comprising active matrixing means including active amplifiers configured for matrix processing L and R stereophonic signals for deriving (L + R) and (L-R) signals.

3. The expansion circuit of claim 1 wherein the active matrixing means includes circuitry for adding and/or subtracting signals in proportions for deriving L and R signals having predetermined characteristics.

4. The expansion circuit of claim 2 wherein the active matrixing means includes circuitry for adding and/or subtracting signals in proportions for deriving (L + R) and (L-R) signals having predetermined characteristics.

5. A matrixing signal processing circuit comprising active matrixing means including active amplifiers having gain and arranged in a matrixing configuration.

6. The circuit of claim 5 wherein at least two of the active matrixing amplifiers are signal coupled in seriatum.

7. A stereophonic expansion circuit comprising active matrixing
20 means including active amplifiers signal coupled in seriatum, configured for
producing a stereo expansion effect.

8. The expansion circuit of claim 7 wherein the active matrixing means includes circuitry for adding and/or subtracting signals in proportions for deriving (L + R) and (L-R) signals having predetermined characteristics.